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06-30-03
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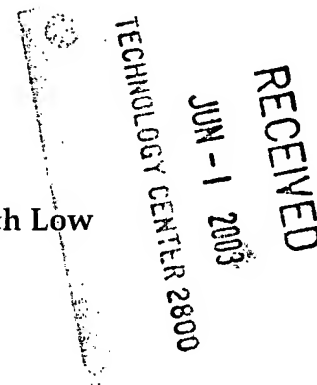
June 26, 2003

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VIA USPS EXPRESS MAIL
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Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

Re: Patent Application for:
"ESD Improvement by a Vertical Bipolar Transistor with Low
Breakdown Voltage and High Beta"
Serial No.: 10/051,639
Attorney Docket: TI-32536
Our File: 50000.2201



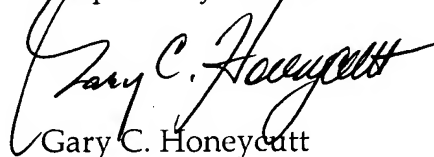
Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Amendment Pursuant to 37 CFR § 1.115;
- (2) Replacement Pages; and
- (3) Postcards.

Please return the date-stamped postcards to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Respectfully submitted,


Gary C. Honeycutt

GCH:glc

Enclosures

cc: W. James Brady, III (Texas Instruments Incorporated, w/ encls.)